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Applicant(s): KAMALESH K. SRIVASTAVA ET AL.

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THERESA T. DOANGroup Art Unit
2814

Invention: METHOD FOR FORMING ROBUST SOLDER INTERCONNECT STRUCTURES BY REDUCING EFFECTS OF SEED LAYER

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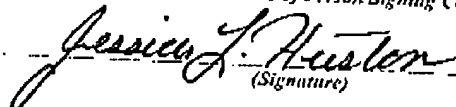
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